NOTICE OF REVISION (NOR) THIS REVISION DESCRIBED BELOW HAS BEEN AUTHORIZED FOR THE DOCUMENT LISTED. 1. DATE (YYMMDD) 95-10-10							Form Approved OMB No. 0704-0188
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Office of Manage PLEASE DO NO ISSUING CONTR	THE GOVERNMENT	3. DODAAC					
4. ORIGINAT	OR		Defense El;ed	Street, City, State, Zipetronics Supply Center		5. CAGE CODE 67268	6. NOR NO. 5962-R001-96
a. TYPED NA Last)	AME (First,	Middle Initial,	1507 Wilming Dayton, OH 4			7. CAGE CODE 67268	8. DOCUMENT NO. 83024
	CUIT, LIN	NT EAR, 4-CHANNEL,	OPTICALLY CO	UPLED	10. REVISION LETT	ER	11. ECP NO.
ISOLATOR,	, HYBRID				a. CURRENT F	b. NEW G	N/A
12. CONFIGU	JRATION	ITEM (OR SYSTEM) TO WHICH EC	CP APPLIES	l	I	l
13. DESCRIF	PTION OF	REVISION					
F F F	Revisions Revisions Revision le Rev status	Itr column; add "G description colum date column; add evel block; add "G s of sheets; for she	n; add "Chang "95-10-10" ". eets 1, 2, and 9		ith NOR 5962-R001	-96".	
	01	6N140A	4-cha	annel optical couple	r		
9	Substitute	wiht the following	:				
()1	6N140A, HCP	L-6571 4-	channel optical cou	pler		
Sheet 2: F	Paragraph	1.2.2.; Add the f	ollowing:				
F	=	CDFP4-F16	16	Flat pack			
	ECTION F	OR GOVERNME	NT USE ONL	Y			
a. (X one)	Х	(1) Existing docu	ment suppleme	ented by the NOR r	nay be used in manu	facture.	
		(2) Revised docu	ment must be	received before ma	nufacturer may incor	porate this change.	
		. ,			e revision and furnis		
	b. ACTIVITY AUTHORIZED TO APPROVE CHANGE FOR GOVERNMENT c. TYPED NAME (First, Middle Initial, Last)						
d. TITLE Chief,				e. SIGNATURE			f. DATE SIGNED (YYMMDD)
,	s Compo	nent Branch		Kendall A. Cotto	ongim		95-10-10
15a. ACTIV	ITY ACC	OMPLISHING RE	VISION	b. REVISION CO	MPLETED (Signatur	e)	c. DATE SIGNED (YYMMDD)
DESC-ELDT Steve L. Duncan				n		95-10-10	

13. DESCRIPTION OF REVISION - CONTINUED	Document No.: 83024 Revision: G NOR No.: 5962-R001-96 Sheet: 2 of 2
Sheet 2: Paragraph 1.3 Absolute maximum ratings: Under Thermal resistance, junction-to-case Delete Case outline E See, MIL-STD-1835 and replace with Case outlines E and F See, MIL-STD-1835.	(O ^{JC})
Sheet 9: FIGURE 2. <u>Terminal connections</u> .	
In the Case outline block delete the E, X, Y, and Z and replace with F, E, X, Y, and Z.	
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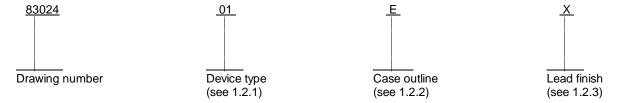
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LTR	DESCRIPTION									DATE (YR-MO-DA)			۹)	APPROVED					
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В	Reinstate changes												90-0)1-10			W. He	eckma	ın
С	Docume current N					inges t	hrougl	nout. (Chang	ed to r	eflect		90-0	08-09			W. He	eckma	เท
D	Added c	ase outli	nes X a	and Y.	Edito	rial cha	anges t	hrough	hout.				93-0)8-31		ŀ	<. A. C	ottong	gim
E	Changes	s in acco	rdance	with N	NOR 5	962-R	032-94	١.					93-1	1-12		ŀ	(. A. C	ottono	gim
F	Add case	e outline	Z. Re	write e	ntire d	ocume	ent.						94-0)4-07		ŀ	(. A. C	ottono	min
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OF

11

- 1. SCOPE
- 1.1 Scope. This drawing describes device requirements for class H hybrid microcircuits to be processed in accordance with MIL-H-38534.
 - 1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:



1.2.1 <u>Device type(s)</u>. The device type(s) shall identify the circuit function as follows:

Device type	Generic number	<u>Circuit function</u>
01	6N140A	4-channel optical coupler

1.2.2 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style		
E	CDIP2-T16	16	Dual-in-line		
Χ	See figure 1	16	Dual-in-line		
Υ	See figure 1	16	Dual-in-line		
Z	See figure 1	16	Dual-in-line		

- 1.2.3 Lead finish. The lead finish shall be as specified in MIL-H-38534. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.
 - 1.3 Absolute maximum ratings. 1/

Supply voltage (V _{CC})	-0.5 V dc to +20 V dc 2/
Peak input current (each channel, ≤ 1 ms duration)	20 mA
Average input current, I _F (each channel)	10 mA <u>3</u> /
Reverse input voltage, V _R (each channel)	5 V dc
Output current, IO (each channel)	40 mA
Output voltage, VO (each channel)	-0.5 V dc to +20 V dc 2/
Output power dissipation (each channel)	50 mW <u>4</u> /
Storage temperature	-65°C to +150°C
Junction temperature (T _J)	+175° C
Lead solder temperature (soldering, 10 seconds)	+260°C (1.6 mm below seating plane)
Thermal resistance, junction-to-case (θ_{JC})	
Case outline E	See MIL-STD-1835
Case outlines X, Y, and Z	+28° C/W
Case temperature	+170°C

- Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
- Pin 10 should be the most negative voltage at the detector side.

Keeping V_{CC} as low as possible, but greater than 2.0 volts, will provide the lowest total I_{OH} over temperature. 3/ Derate I_F at 0.33 mA/° C above +110° C.

- 4/ Output power is collector output power plus one fourth of total supply power. Derate at 1.66 mW/°C above +110°C.

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	1.4	Recommended	operating	conditions.
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Supply voltage range	2.0 V dc minimum to 18 V dc maximum
High level input current	0.5 mA dc minimum (each channel) up to 5 mA dc maximum
Low level input voltage	0.8 V maximun (each channel)
Ambient operating temperature range (T_{Δ})	-55° C to +125° C

2. APPLICABLE DOCUMENTS

2.1 <u>Government specification and standards</u>. Unless otherwise specified, the following specification and standards of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-H-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

MIL-STD-1835 - Microcircuit Case Outlines.

(Copies of the specification and standards required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.
- 3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.
 - 3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein and figure 1.
 - 3.2.2 <u>Terminal connections</u>. The terminal connections shall be as specified on figure 2.
- 3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.
- 3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.
- 3.5 <u>Marking</u>. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534 (see 6.6 herein).
- 3.6 <u>Manufacturer eligibility</u>. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DESC-EC) upon request.

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		TABLE I. Electrical performand	ce characterist	ics.			
Test	Symbol	Conditions -55° C ≤ T _A ≤ +125° C	Group A subgroups	Device type	Limits	Unit	
	unless otherwise specified				Min	Max	
Low level output voltage	V _{OL}	$V_{CC} = 4.5 \text{ V; I}_F = 0.5 \text{ mA;}$ $I_{OL} = 1.5 \text{ mA} \frac{1}{4}$	1, 2, 3	01		0.4	V
		V _{CC} = 4.5 V; I _F = 5 mA; I _{OL} = 10 mA <u>1</u> /				0.4	
Current transfer ratio	h _{F(CTR)}	$V_O = 0.4 \text{ V; I}_F = 0.5 \text{ mA;}$ $V_{CC} = 4.5 \text{ V} \frac{1}{2}$	1, 2, 3	01	300		%
		$V_O = 0.4 \text{ V; I}_F = 1.6 \text{ mA;}$ $V_{CC} = 4.5 \text{ V } \underline{1/2/}$			300		
		V _O = 0.4 V; I _F = 5 mA; V _{CC} = 4.5 V <u>1</u> / <u>2</u> /			200		
High level output current	ГОН	V _{CC} = 18 V; V _O = 18 V; I _F = 2 μΑ <u>1</u> / <u>3</u> /	1, 2, 3	01		250	μA dc
High level supply current	I _{CCH}	V _{CC} = 18 V; I _{F1} = I _{F2} = I _{F3} = I _{F4} = 0 mA	1, 2, 3	01		40	μA dc
Low level supply current	I _{CCL}	$V_{CC} = 18 \text{ V};$ $I_{F1} = I_{F2} = I_{F3} = I_{F4} = 1.6 \text{ mA}$	1, 2, 3	01		4	mA dc
Input forward voltage	V _F	I _F = 1.6 mA <u>1</u> /	1, 2	01		1.7	V dc
Input reverse breakdown voltage	V _{BR}	I _R = 10 μA <u>1</u> /	1, 2, 3	01	5.0	1.0	V dc
Input to output insulation leakage current	I _{IO}	V _{IO} = 1500 V dc; <u>4/</u> Relative humidity = 45%, t = 5 seconds; T _A = +25° C	1	01		1.0	μA dc
Capacitance between input-output	C _{IO}	f = 1 MHz; T _A = +25°C, 1/5/6/	4	01		4	pF

See footnotes at end of table.

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TABLE I.	Electrical performa	ance characteristics	 Continued.

Test	Symbol	Conditions Group A -55° C \leq T _A \leq +125 $^{\circ}$ C subgroups		Device type	Limits		Unit
		unless otherwise specified			Min	Max	
Propagation delay time, low to high output level	t _{PLH}	$I_F = 0.5 \text{ mA}; R_L = 4.7 \text{ k}\Omega;$ $V_{CC} = 5.0 \text{ V} \underline{1}\text{/}$	9,10,11	01		60	μs
		$I_F = 5 \text{ mA}; R_L = 680\Omega;$	9	01		20	μs
		$\dot{V}_{CC} = 5.0 \text{ V}^{-1}/$	10,11			30	
Propagation delay time, high to low output level	t _{PHL}	$I_F = 0.5 \text{ mA}; \text{ RL} = 4.7 \text{ k}\Omega;$ $V_{CC} = 5.0 \text{ V} \text{ 1/}$	9,10,11	01		100	μs
		$I_{F} = 5 \text{ mA}; R_{L} = 680\Omega;$	9	01		5	μs
		$\dot{V}_{CC} = 5.0 \text{ V}^{-1}/$	10,11			10	
Common mode transient immunity at high output level	C _{MH}	V_{CM} = 25 V (peak); V_{CC} = 5.0 V; R _L = 1.5 kΩ; I_F = 0 mA; <u>1</u> / <u>6</u> / <u>7</u> / <u>8</u> /	9,10,11	01	500		V/µs
Common mode transient immunity at low output level, per channel	C _{ML}	V_{CM} = 25 V (peak); V_{CC} = 5.0 V; R _L = 1.5 kΩ; I_F = 1.6 mA; <u>1</u> / <u>6</u> / <u>8</u> / <u>9</u> /	9,10,11	01	500		V/µs

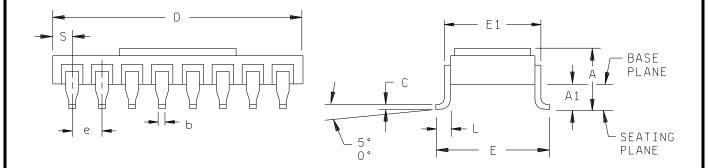
- Each channel.
- Current transfer ratio is defined as the ratio of output collector current I_O, to the forward LED input current, I_F, times 100
- $I_F = 2 \mu A$ for channel under test. For all other channels, $I_F = 10 \text{ mA}$.
- Device considered a two-terminal device. Pins 1 through 8 are shorted together and pins 9 through 16 are shorted together.
- Measured between the LED anode and cathode shorted together and pins 10 through 15 shorted together.
- Parameters shall be tested as part of device initial characterization and after design and process changes. Parameters shall be guaranteed to the limits specified in table I for all lots not specifically tested.
- <u>7</u>/ CM_H is the maximum tolerable common mode transient to assure that the output will remain in a high logic state (i.e., $V_O > 2.0 \text{ V}$).
- In applications where dV/dt may exceed 50,000 V/ μ s (such as a static discharge) a series resistor, R_{CC}, should be included to protect the detector IC's from destructively high surge currents. The recommended value is: $R_{cc} = \frac{1}{0.6} \frac{V}{I_F(mA)} k\Omega$

$$R_{cc} = \frac{1 V}{0.6 I_{E}(mA)} k\Omega$$

 CM_L is the maximum tolerable common mode transient to assure that the output will remain in a low logic state (i.e., V_O <

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Case outline X



Symbol	Millimeters		Inches	3
	Min	Max	Min	Max
A		4.57		0.180
A1	1.40	1.65	0.055	0.065
b	0.41	0.51	0.016	0.020
C	0.18	0.33	0.007	0.013
D	20.07	20.83	0.790	0.820
e	2.29	2.79	0.090	0.110
E	9.65	9.91	0.380	0.390
E1		8.13		0.320
L	1.07	1.32	0.042	0.052
S	0.89	1.52	0.035	0.060

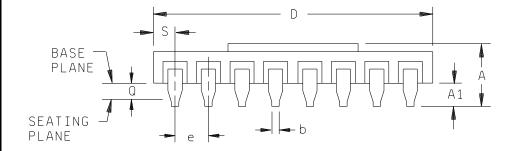
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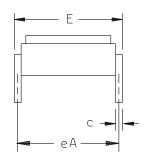
- 1. The U.S. Government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
- 2. Pin 1 is indicated by a dot marked on top of the package.

FIGURE 1. Case outlines.

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Case outline Y





Symbol	Millimeters		Inches	3
·	Min	Max	Min	Max
A		4.32		0.170
A1	1.14	1.40	0.045	0.055
b	0.41	0.51	0.016	0.020
С	0.18	0.33	0.007	0.013
D	20.07	20.83	0.790	0.820
е	2.29	2.79	0.090	0.110
		8.13		0.320
eA	7.37	7.87	0.290	0.310
Q	0.51		0.020	
S	0.89	1.52	0.035	0.060

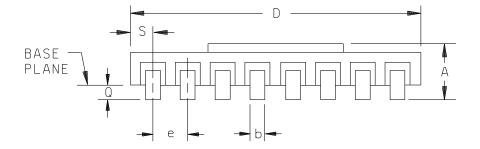
NOTES:

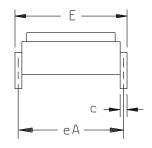
- 1. The U.S. Government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule. Pin 1 is indicated by a dot marked on top of the package.

FIGURE 1. Case outlines - Continued.

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Case outline Z





Symbol	Millimeters		Inches	3
	Min	Max	Min	Max
A		3.56		0.140
b	0.89	1.14	0.035	0.045
С	0.18	0.33	0.007	0.013
D	20.07	20.83	0.790	0.820
e	2.29	2.79	0.090	0.110
E		8.13		0.320
eA	7.37	7.87	0.290	0.310
Q	0.51		0.020	
S	0.89	1.52	0.035	0.060

NOTES:

- The U.S. Government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
- 2. Pin 1 is indicated by a dot marked on top of the package.

FIGURE 1. Case outlines - Continued.

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Device type	01
Case outlines	E, X, Y, and Z
Terminal number	Terminal connection
1	Cathode 1
2	Anode 1
3	Anode 2
4	Cathode 2
5	Cathode 3
6	Anode 3
7	Anode 4
8	Cathode 4
9	NC
10	GND
11	Output 4
12	Output 3
13	Output 2
14	Output 1
15	V _{CC}
16	NC

FIGURE 2. <u>Terminal connections</u>.

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TABLE II. Electrical test requirements.

MIL-H-38534 test requirements	Subgroups (in accordance with MIL-H-38534, group A test table)
Interim electrical parameters	1
Final electrical test parameters	1*, 2, 3, 9
Group A test requirements	1, 2, 3, 4, 9, 10, 11
Group C end-point electrical parameters	1, 2, 3

^{*} PDA applies to subgroup 1.

- 3.7 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in QML-38534 (see 6.6 herein). The certificate of compliance submitted to DESC-EC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.
- 3.8 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.
 - 4. QUALITY ASSURANCE PROVISIONS
 - 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-H-38534.
 - 4.2 Screening. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DESC-EC or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.
 - 4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 5, 6, 7, and 8 shall be omitted.
 - 4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.

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- 4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DESC-EC or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.
- 4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.
- 5. PACKAGING
- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.
- 6. NOTES
- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.
- 6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-973 using DD Form 1692, Engineering Change Proposal.
- 6.4 <u>Record of users</u>. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-EC, telephone (513) 296-6047.
 - 6.5 Comments. Comments on this drawing should be directed to DESC-EC, Dayton, Ohio 45444, or telephone (513) 296-5373.
- 6.6 <u>Approved sources of supply</u>. Approved sources of supply are listed in QML-38534. Additional sources will be added to QML-38534 as they become available. The vendors listed in QML-38534 have agreed to this drawing and a certificate of compliance (see 3.7 herein) has been submitted to and accepted by DESC-EC.

STANDARDIZED		
MILITARY DRAWING		
DEFENSE ELECTRONICS SUPPLY CENTER		
DAYTON, OHIO 45444		

SIZE A		83024
	REVISION LEVEL F	SHEET 14

STANDARDIZED MILITARY DRAWING SOURCE APPROVAL BULLETIN

DATE: 94-04-07

Approved sources of supply for SMD 83024 are listed below for immediate acquisition only and shall be added to QML-38534 during the next revision. QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DESC-EC. This bulletin is superseded by the next dated revision of QML-38534.

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN <u>1</u> /
8302401EX	31757 50434	6N140/883B 6N140A/883B
8302401XX	50434	6N140A/883B#300
8302401YX	50434	6N140A/883B#100
8302401ZX	50434	6N140A/883B#600

1/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE <u>number</u>	Vendor name <u>and address</u>	
31757	Micropac Industries, Incorporated Optoelectronics Division 905 E. Walnut Street Garland, TX 75040-6611 Point of contact: Optoelectronic Division 725 E. Walnut Street Garland, TX 75040	
50434	Hewlett Packard Optical Communications Division 370 West Trimble Road San Jose, CA 95131-1096 Point of contact: Optoelectronic Division 350 West Trimble Road San Jose, CA 95131-1096	

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in this information bulletin.